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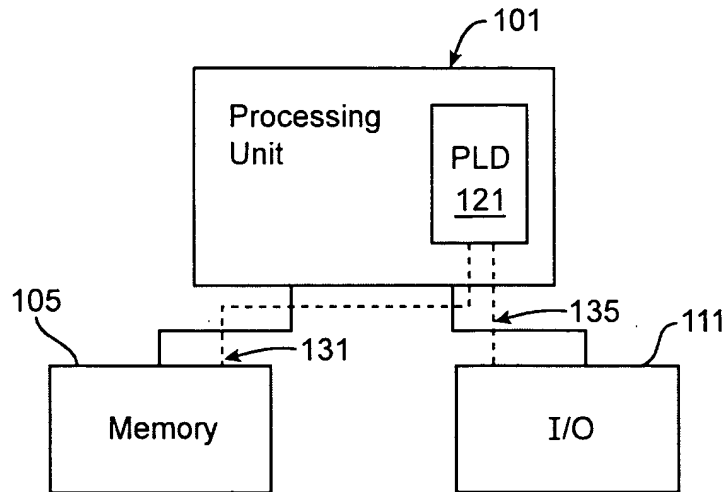


FIG. 1

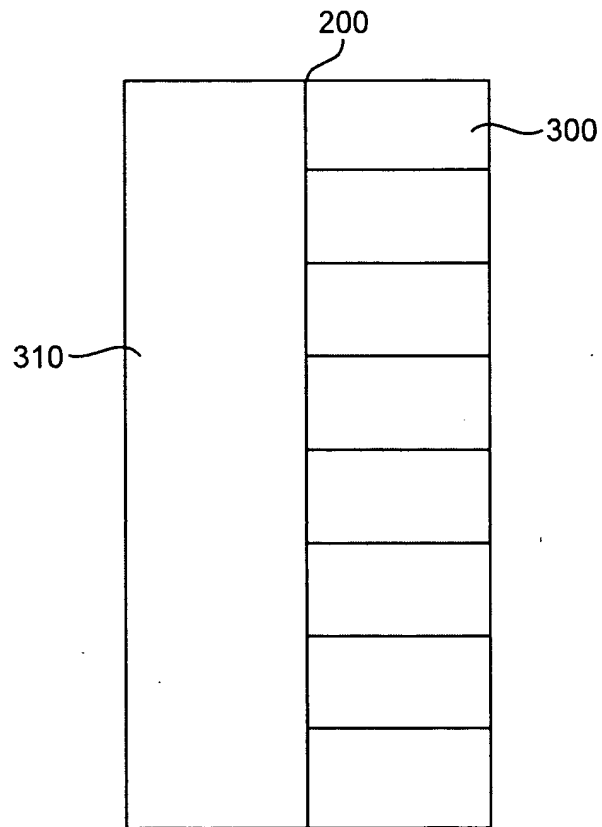


FIG. 3



FIG. 2

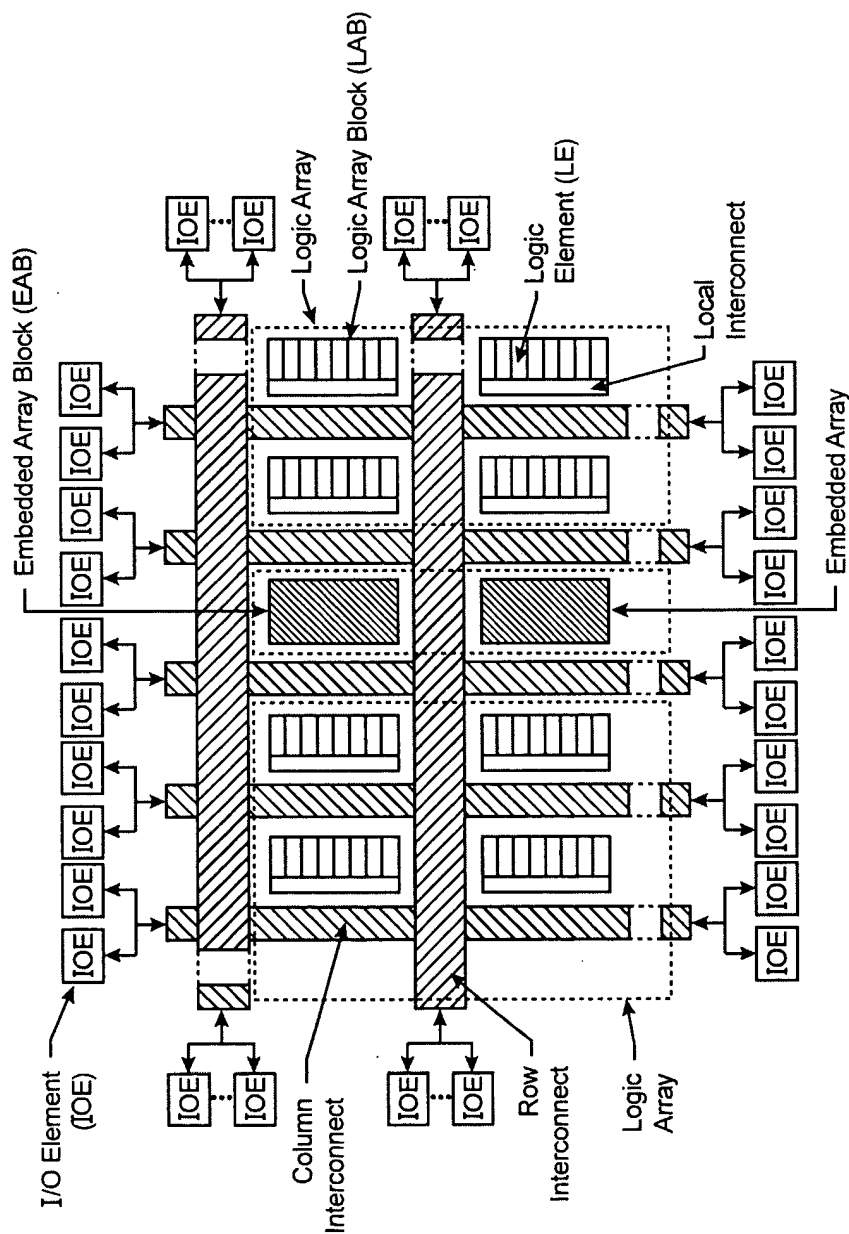


FIG. 4



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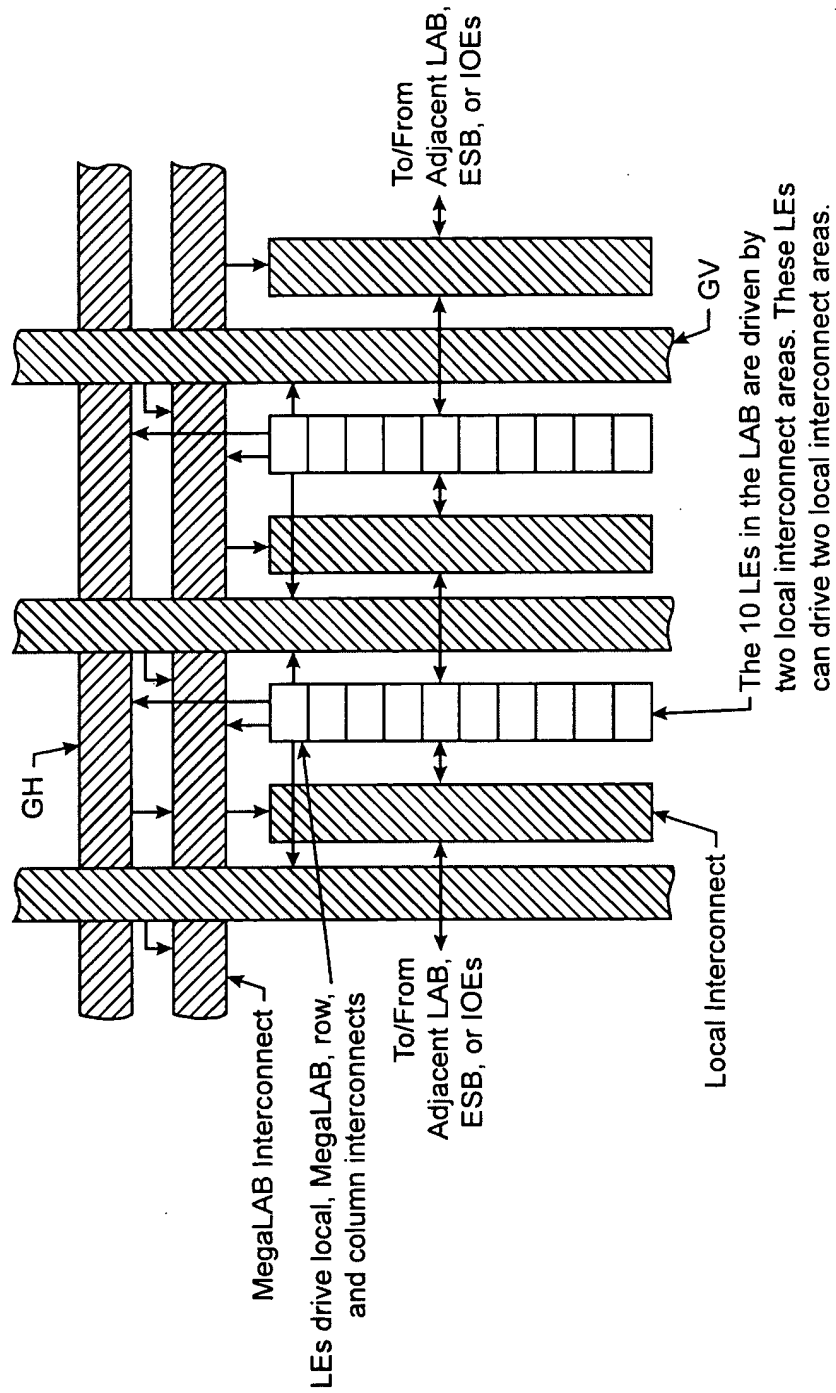


FIG. 5

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represents on-chip termination

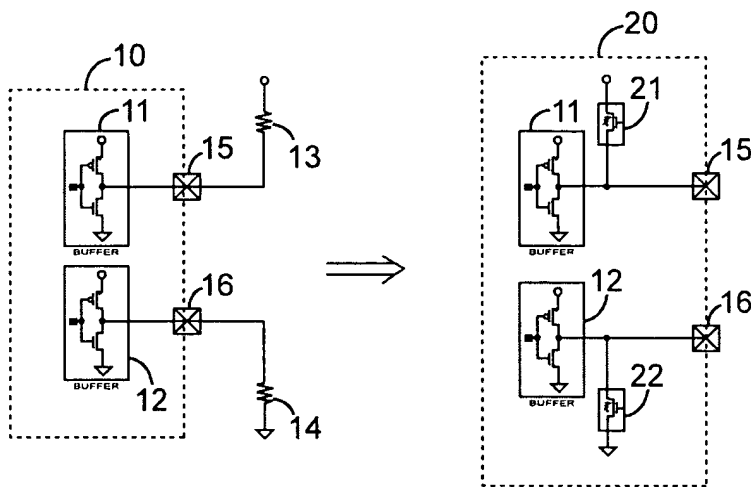


FIG. 6A

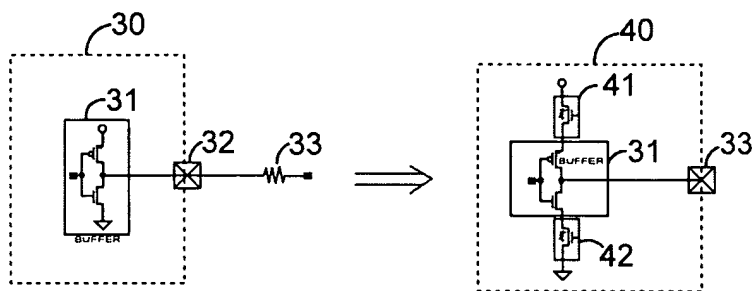


FIG. 6B

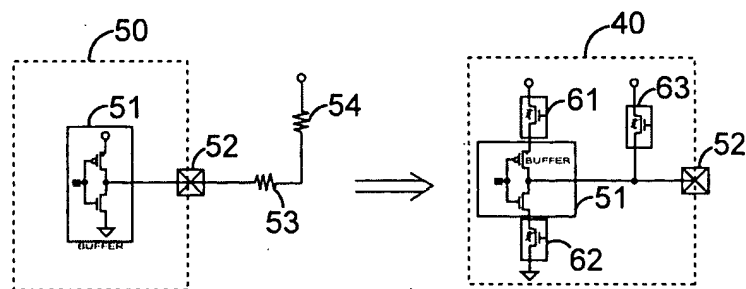


FIG. 6C

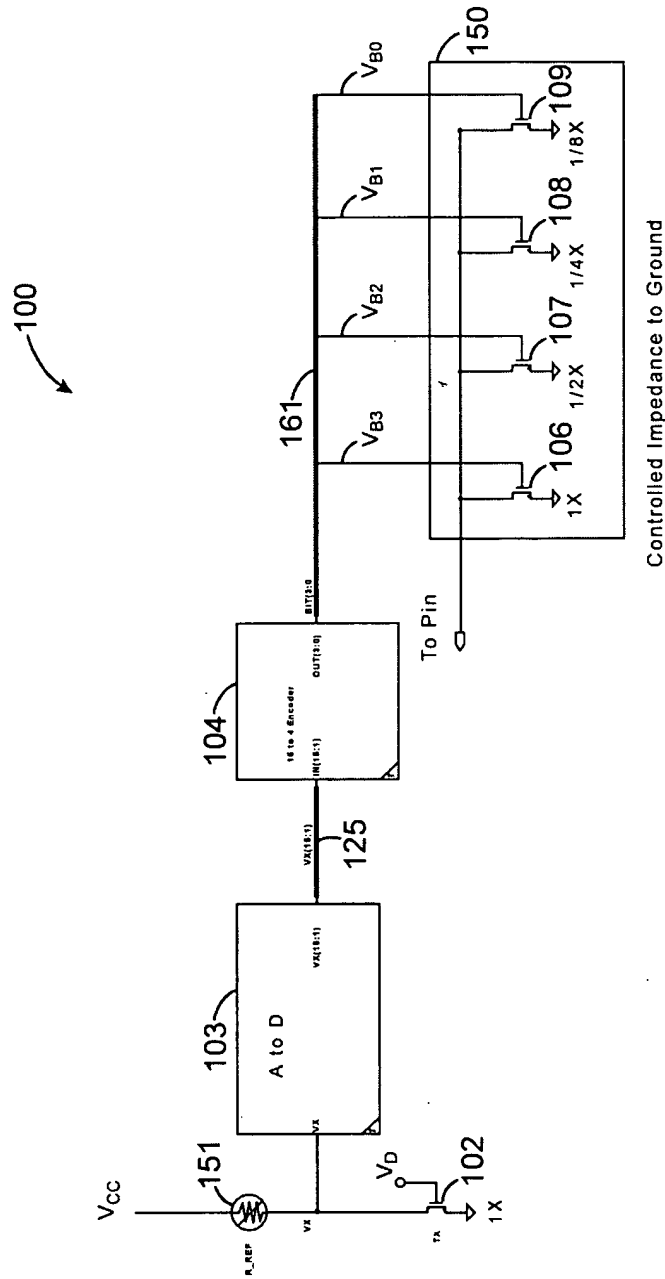


FIG. 7



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15-bit A to D converter

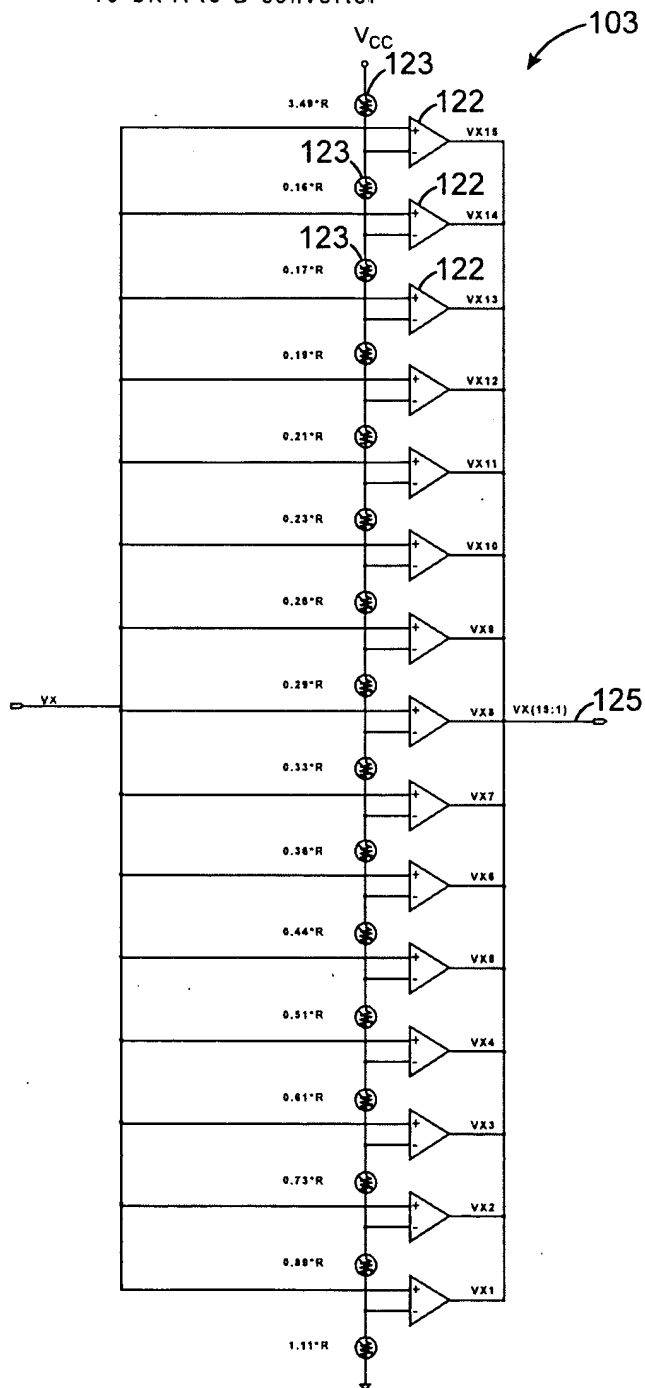


FIG. 8

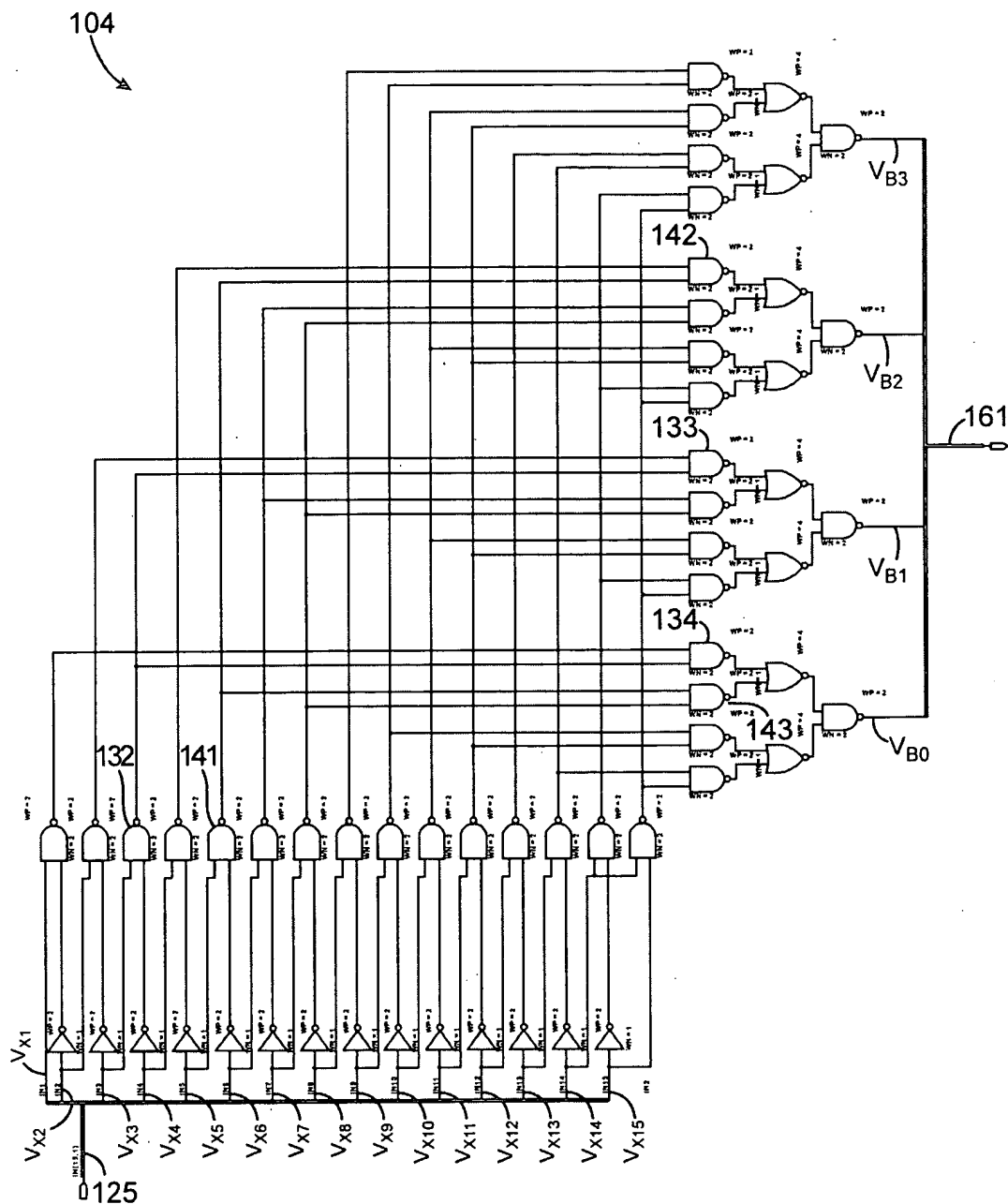


FIG. 9